

Stecker 1 ECB-Bus

5c A0	2	IC 1	18	AB0	3
7c A1	17		3	AB1	3
6a A2	4		16	AB2	3
6c A3	15		5	AB3	3
7a A4	6		14	AB4	3
8a A5	13	74 HC	7	AB5	3
9a A6	8	244	12	AB6	3
9c A7	11		9	AB7	3

8c A8	2	IC 2	18	AB8	3
30a A9	17		3	AB9	3
18c A10	4		16	AB10	3
17c A11	15		5	AB11	3
27c A12	6		14	AB12	3
29a A13	13	74 HC	7	AB13	2,3
18a A14	8	244	12	AB14	2,3
28c A15	11		9	AB15	2

2c D0	2	IC 3	18	DB0	3
14c D1	3		17	DB1	3
4c D2	4		16	DB2	3
4a D3	5		15	DB3	3
5a D4	6		14	DB4	3
2a D5	7	74 HC	13	DB5	3
3a D6	8	245	12	DB6	3
3c D7	9		11	DB7	3

10c A16	2	IC 4	18	AB16	2
12c A17	17		3	AB17	2
13c A18	4		16	AB18	2
28a RFSH	15		5	RFSHB	2
24c R	6				
24c R	13	74 HC	7	RB	3
22c W	8	244	12	WB	3
30c MREQ	11		9	MREQB	2

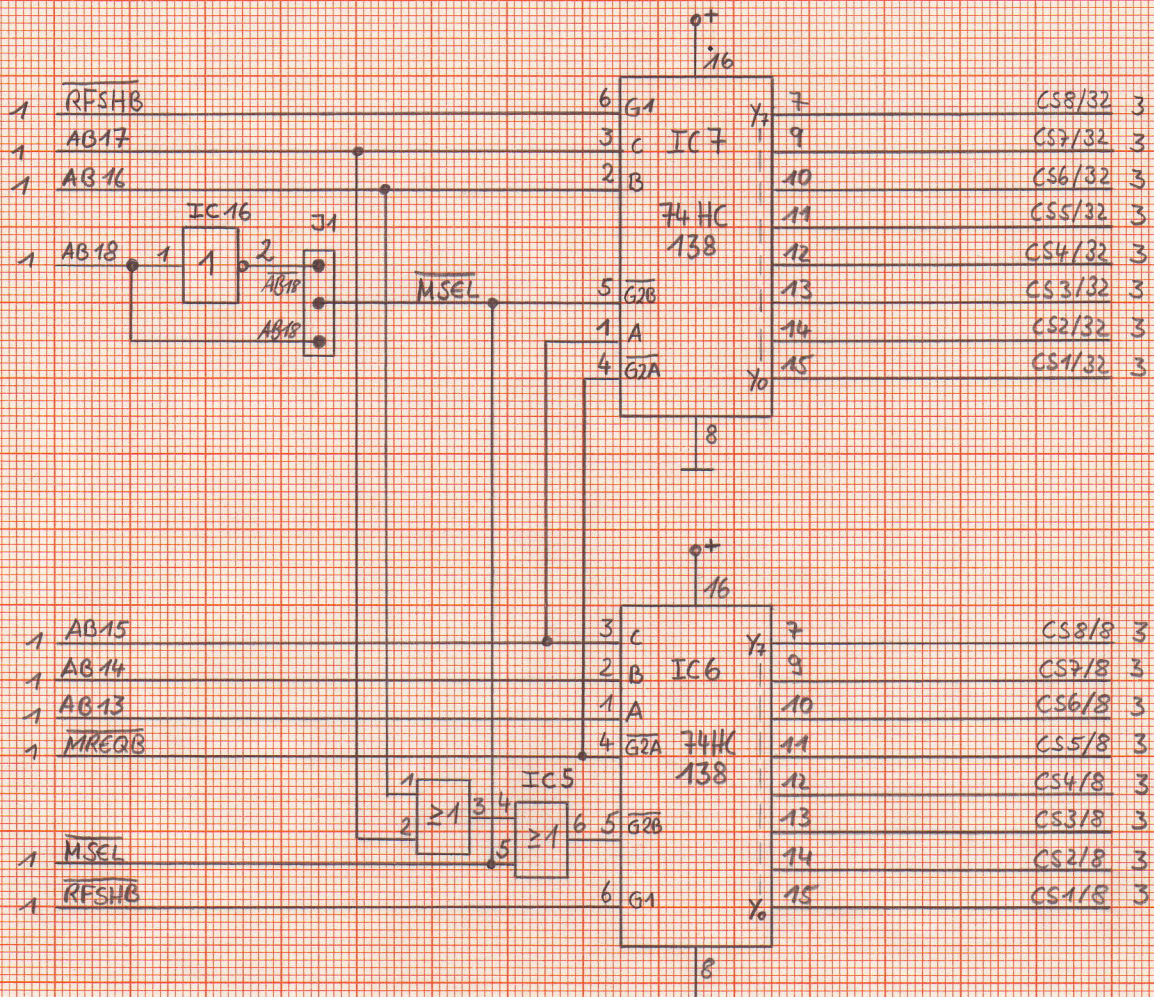
Array
4x100K.2

Speicherkarte 256K HD64180

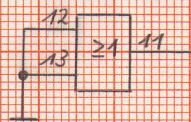
02.12.87 Kai

BUSPufferung

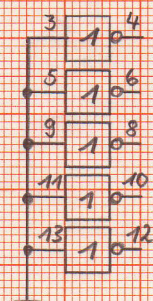
BLATT 1/5



IC5: 74HC32



IC16: 74HC04

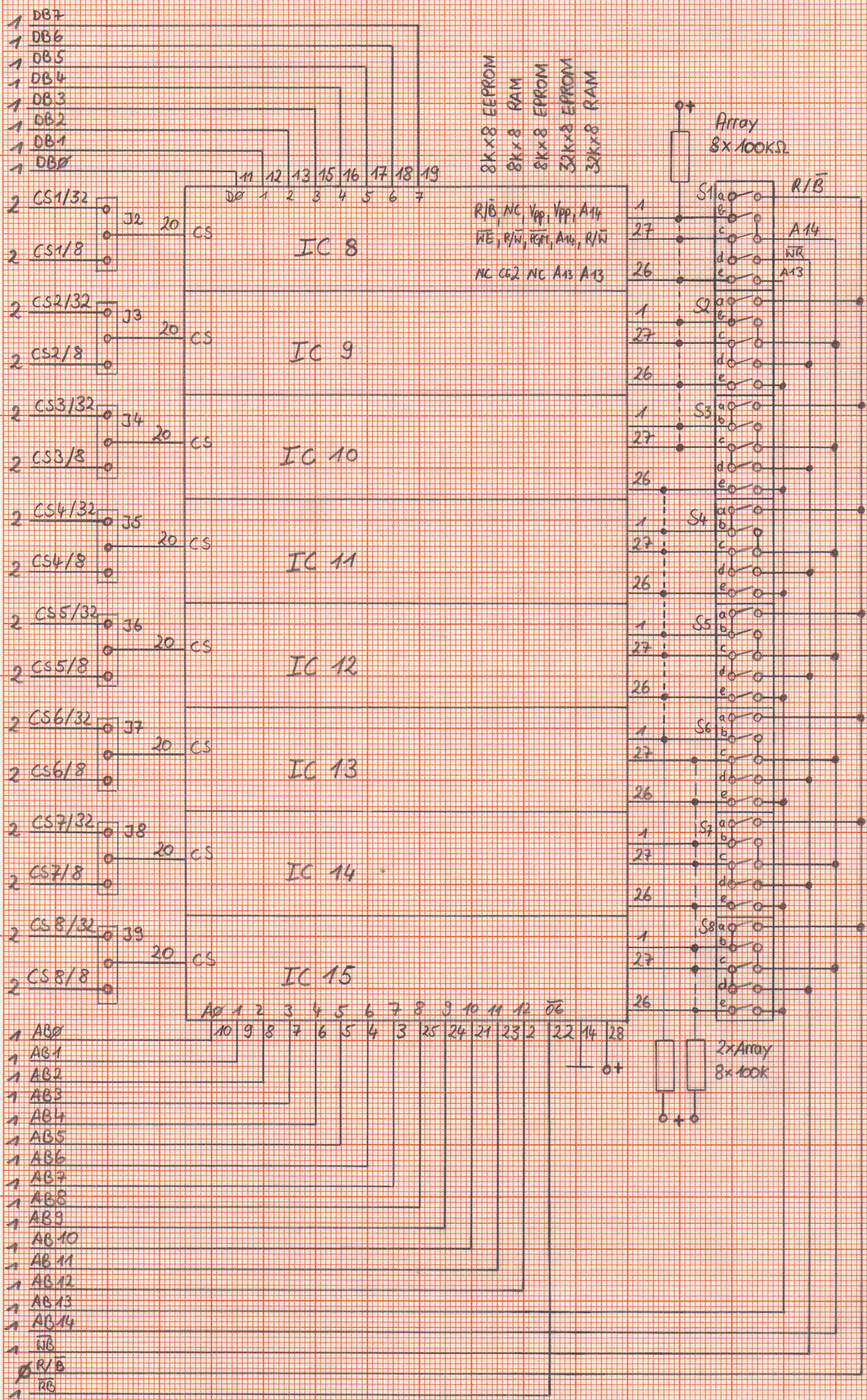


Speicherkarte 256K HD64180

Decodierung

07.12.87 Kii

BloH2/5



03.12.87 / m

Speicherkarte 256k

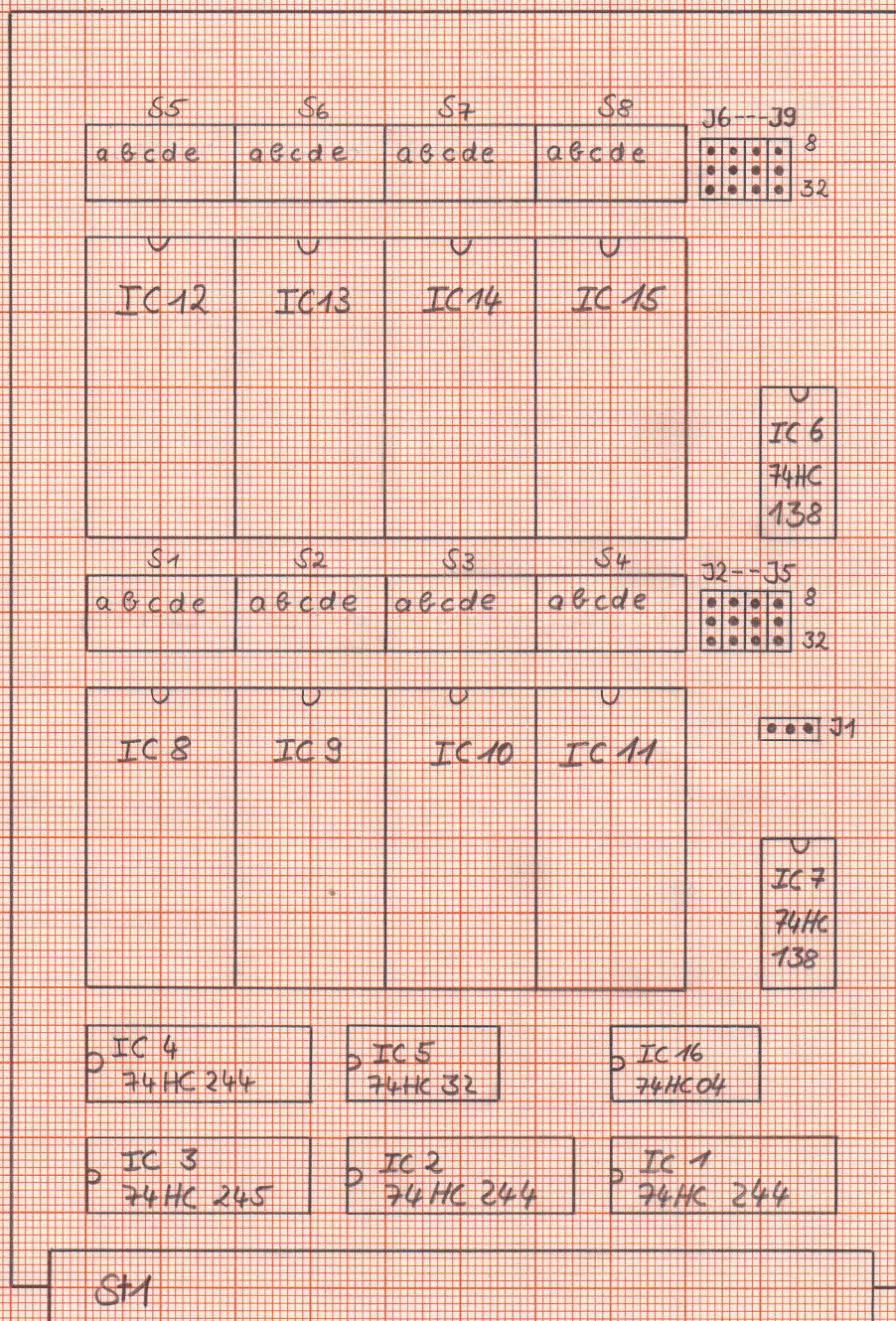
Blatt 3/5

Speicherkarte 256k : "DP-Schalter"

	S _n a	S _n b	S _n c	S _n d	S _n e
8kx8 EEPROM	X			X	
8kx8 RAM				X	
8kx8 EPROM					
32kx8 EPROM			X		X
32kx8 RAM		X		X	X

X $\hat{=}$ Schalter ein!

BESTÜCKUNGSPLAN: „Speicherkarte 256k“



03.12.87
Vux

Blatt 5/5